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Faxc	xc (571) 273-8300			Pages:	6 pages including cover sheet		
Phone	:			Date:	11/21/2005		
Re:	U.S. Serial No: 10/709,239 Group Art Unit: 2818 Confirmation No: 3238 Docket No: FIS920030375US1 (17192)			CC:			
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Application No. 10/709,239	Filing Date April 23, 2004	Examiner Calvin Lee		Group Art Unit 2818		
Invention: STRUCTURES AND METHODS FOR MANUFACTURING OF DISLOCATION FREE STRESSED CHANNELS IN BULK SILICON AND SOI CMOS DEVICES BY GATE STRESS ENGINEERING WITH SiGe AND/OR Si:C						
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TRANSMITTAL LETTER (General - Patent Pending)				Docket No. FIS920030375US1 (17192)		
In Re Application Of: Huilong Zhu, et al.						
Application No. 10/709,239	Filing Date April 23, 2004	Examiner Calvin Lee	Customer No. 23389	Group Art Unit Confirmation No. 2818 3238		
Title: STRUCTURES AND METHODS FOR MANUFACTURING OF DISLOCATION FREE STRESSED CHANNELS IN BULK SILICON AND SOI CMOS DEVICES BY GATE STRESS ENGINEERING WITH SIGE AND/OR SI:C						
		COMMISSIONER FOR PATI	ENTS:			
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RESPONSE UNI	DER 37 C.F.R. §§1.11	1 AND 1.143				
in the above identified application. ☑ No additional fee is required.						
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<u>St</u>	M Deltassignature		Dated: Nove	mber 21, 2005		
Steven Fischman Registration No. 34,594						
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TRANSMITTAL LETTER (General - Patent Pending)				Docket No. FIS920030375US1 (17192)			
					3/3032 (2/2/2)		
in Re Application	In Re Application Of: Huilong Zhu, et al.						
Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.		
10/709,239	April 23, 2004	Calvin Lee	23389	2818	3238		
CHANNEI	Title: STRUCTURES AND METHODS FOR MANUFACTURING OF DISLOCATION FREE STRESSED CHANNELS IN BULK SILICON AND SOI CMOS DEVICES BY GATE STRESS ENGINEERING WITH SIGE AND/OR Si:C						
		COMMISSIONER FOR PATE	ENTS:				
Transmitted herew	rith is:						
	DER 37 C.F.R. §§1.11	1 AND 1.143					
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in the above identi	ified application.						
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	Signature		Dated: Nove	mber 21, 2005			
Steven Fischman							
Registration No. 34,	,594		Thereby certify	that this corresp	pondence is being		
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P18A/REV03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Huilong Zhu, et al.

Examiner: Calvin Lee

Serial No: 10/709,239

Art Unit: 2818

Filed: April 23, 2004

Docket: FIS920030375US1 (17192)

For: STRUCTURES AND METHODS FOR

MANUFACTURING OF DISLOCATION FREE STRESSED CHANNELS IN BULK SILICON AND SOI CMOS DEVICES BY GATE STRESS ENGINEERING WITH Dated: November 21, 2005

SiGe AND/OR Si:C

Confirmation No: 3238

Commissioner of Patents and Trademarks P.O. Box 1450 Alexandria, Virginia 22313-1450

RESPONSE UNDER 37 C.F.R. §§1.111 AND 1.143

Sir:

In response to the Office Action dated October 20, 2005, applicants provisionally elect, without traverse, the subject matter of Group II, i.e., Claims 1-13, for continued prosecution in the above-identified patent application.

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Dated: November 21, 2005

Steven Liscinnan

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